Incremental DPD Linearization for Mobile Terminals with Non-Flat Frequency Response in Dynamic Bandwidth Re-Allocation Scenarios

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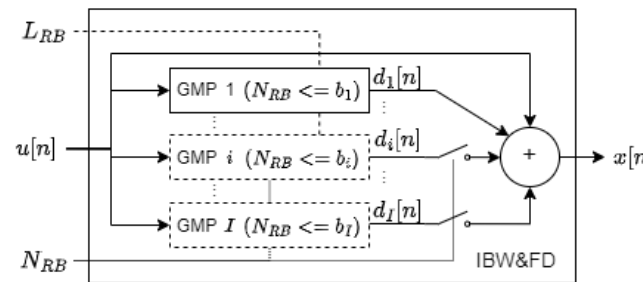
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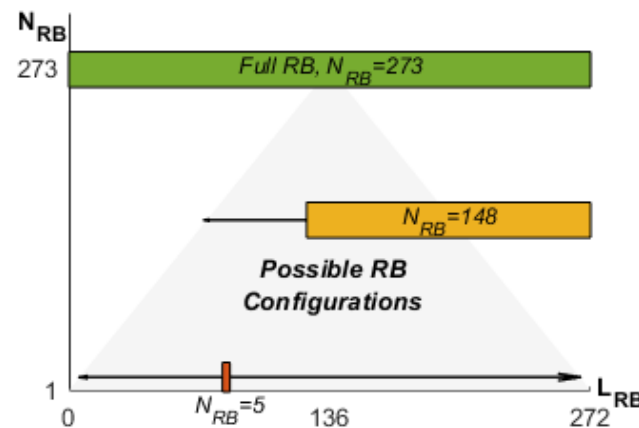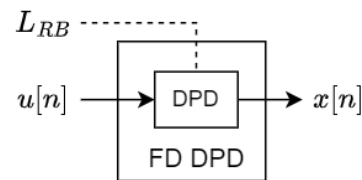Department of Signal Theory and Communications

Components and Systems for
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Outline

- ## Introduction
  - Dynamic RB Allocation in 5G NR Applications.
  - RF PA under Non-Flat Frequency Response.

- ## Incremental Bandwidth (IBW) Linearization
  - Incremental Bandwidth DPD Model
  - Constrained DOMP for IBW GMP Basis Selection

- ## Experimental Setup and Results

- ## Conclusion

Introduction

- 5G NR RB Reallocation Scenario: Dynamic RB location & number, assuming fixed Fc.
- For dynamic RB location (i.e., **frequency**), we propose the **FD DPD** (presented in RWW 2023).
- For dynamic RB number (i.e., **bandwidth**), we propose the **IBW DPD** (**this work**).
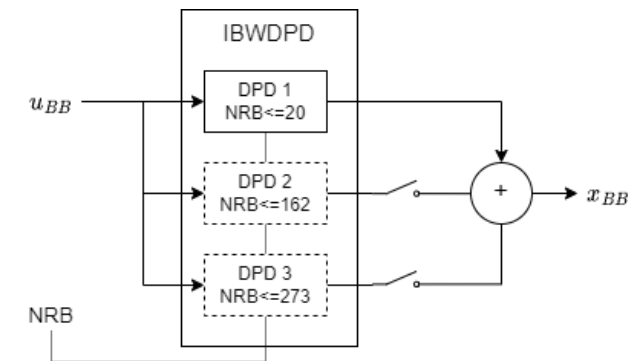
Dynamic Frequency

- For narrow-band signals.
- Less memory effects.
- FD nonlinearity.
- Coefficient interpolatable. (e.g., FDMP/FDP model)
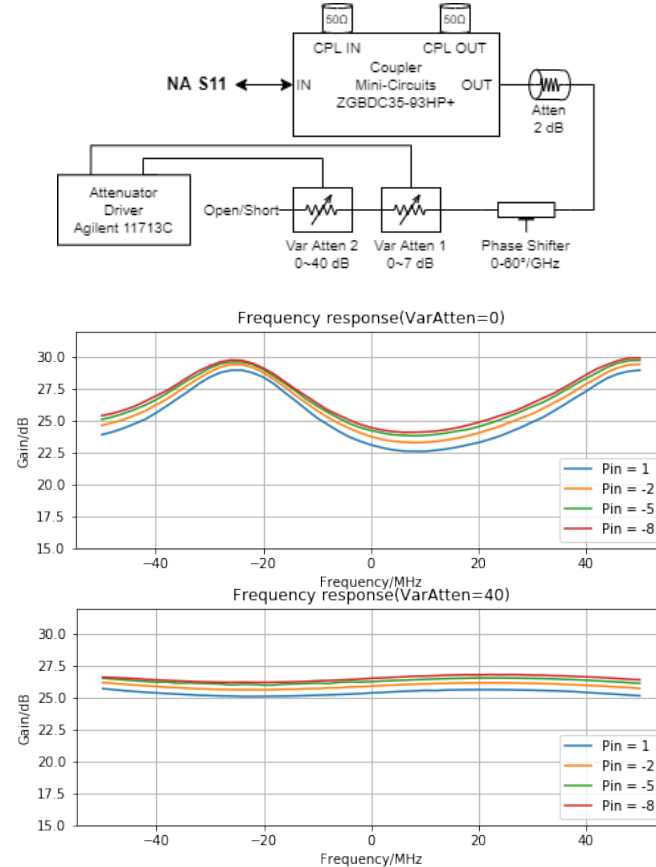

FD DPD

IBW & FD

Dynamic Bandwidth

- Simple model for narrow-band.
- Introduce more basis for wide-band.
- Complexity for implementation.
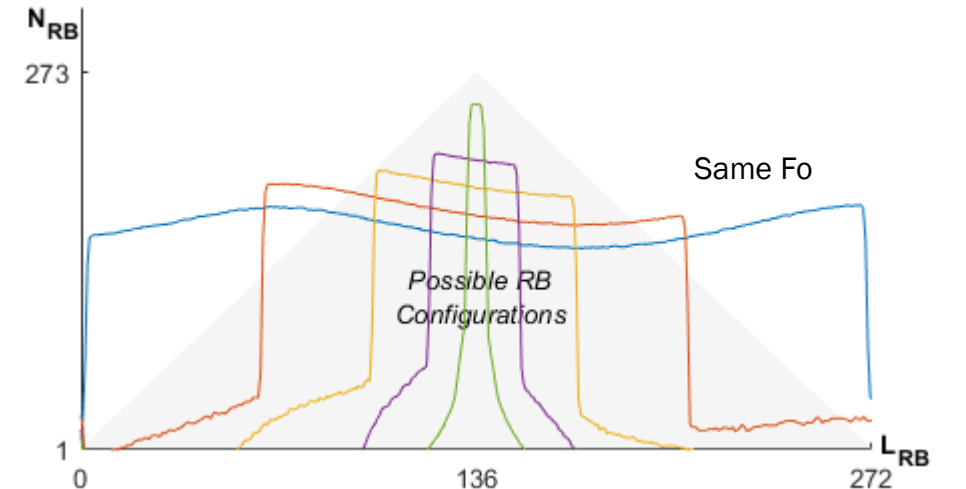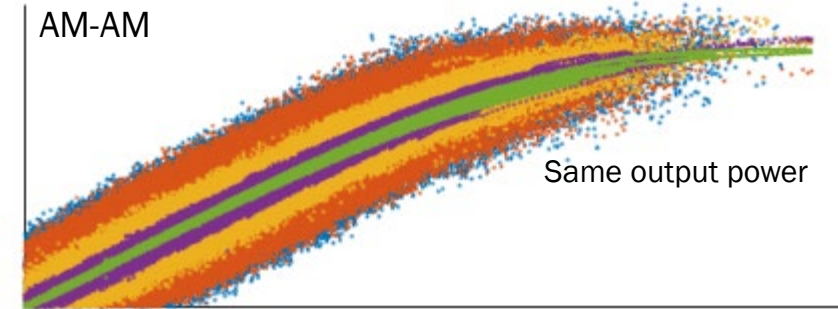- Prefer an incremental model.

Introduction

- Mobile-terminal PAs usually work under large load-mismatched conditions.

Atten = 0 dB, VSWR ≈ 3

Atten = 40 dB, VSWR ≈ 1 (Well-Matched)

Load-mismatched Creation

Consequence of different bandwidth

Incremental Bandwidth DPD Model

Objective: Make the DPD model incremental with bandwidth,
- by appending new basis,
- with fixed coefficients.

Assuming: the additive distortion is orthogonal to the previous basis functions.

Based on GMP:

$$d_i[n] = \sum_{k_i=1}^{K_i} w_{k_i} u[n-\tau_{k_i}] \left| u[n-\tau_{k_i}-\sigma_{k_i}] \right|^{p_{k_i}}$$

Predistortion Signal:

$$x[n] = u[n] - \sum_{i=1}^{I} d_i[n]$$

Narrow-band error:

$$e_1[n] = u[n] - y[n]$$

IBW error:

$$e_i[n] = u[n] - y[n] - \sum_{j=1}^{i-1} d_j[n]; \ (i>1)$$

IBW Coefficients:

$$\boldsymbol{w}_i^{k+1} = \boldsymbol{w}_i^k + \mu (\boldsymbol{U}_i^H \boldsymbol{U}_i)^{-1} \boldsymbol{U}_i^H \boldsymbol{e}_i$$

- Define I bandwidth configurations and the boundaries. (e.g., {10, 40, 80, 162, 273})
- Selects basis incrementally with BW by DOMP.
- Allowing reselection to retrain significant basis.

Algorithm 1 IBW Constrained DOMP

1: **procedure** IBWDOMP(M, U, y)
2: $\quad M^* \leftarrow \{\}, i \leftarrow 1, e \leftarrow y, w \leftarrow ()$
3: $\quad$ **while** $i \leq I$ **do**
4: $\quad\quad M_i \leftarrow \{\}, e_i \leftarrow e_{\{i\}}, Z_i \leftarrow U_{\{i\}}$
5: $\quad\quad$ **repeat**
6: $\quad\quad\quad \varphi^{\{j\}} \xleftarrow{\forall j} \left| \frac{Z_i^{\{j\}}}{\|Z_i^{\{j\}}\|_2} e_i \right|$
7: $\quad\quad\quad j^* \leftarrow \text{pursuit}(M^*, M_i, M, \varphi)$
8: $\quad\quad\quad M_i \leftarrow M_i \cup M^{\{j^*\}}$
9: $\quad\quad\quad \sigma \leftarrow Z_i^H Z_i$
10: $\quad\quad\quad Z_i \leftarrow Z_i - \sigma \otimes Z_i^{\{j^*\}}$
11: $\quad\quad\quad U_* \leftarrow U_i[M_i]$
12: $\quad\quad\quad w_i \leftarrow (U_*^H U_*)^{-1} U_*^H y_i$
13: $\quad\quad\quad e_i \leftarrow y_i - U_* w_i$
14: $\quad\quad$ **until** stopping criterion is met
15: $\quad\quad M^* \leftarrow M^* \cup M_i$
16: $\quad\quad U_* \leftarrow U[M^*]$
17: $\quad\quad w \leftarrow (w^T, w_i^T)^T$
18: $\quad\quad \boxed{e \leftarrow y - U_* w}$ *<- IBW residual error*
19: $\quad\quad M \leftarrow M_{\notin M_i}$ ▷ only if not allowing re-selection
20: $\quad\quad i \leftarrow i + 1$
21: $\quad$ **end while**
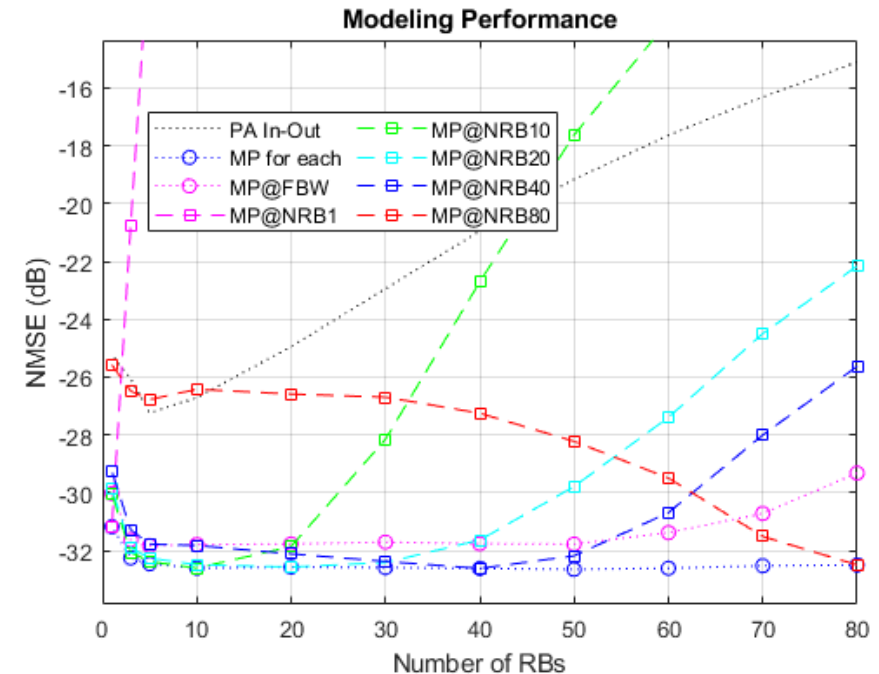22: $\quad$ **return** M^*
23: **end procedure**

** The **i** here represents different BW*

Algorithm 2 Greedy selection with memory preference

1: **procedure** PURSUIT(M^*, M_i, M, φ)
2: $\quad P^* \leftarrow M^{*\{\tau,\sigma\}} \cup M_i^{\{\tau,\sigma\}}, P \leftarrow M^{\{\tau,\sigma\}}$
3: $\quad \varphi_n \leftarrow \frac{\varphi - \min(\varphi)}{\max(\varphi - \min(\varphi))}$
4: $\quad \varphi_s, s_{id} \xleftarrow{\forall \varphi_n > \gamma} \text{sort}_{\text{desc}}(\varphi_n)$ *<- Score threshold*
5: $\quad P_s \leftarrow P^{\{s_{id}\}}$
6: $\quad$ **if** $P_s \cap P^* \neq \emptyset$ **then**   *<- Memory preference*
7: $\quad\quad j \leftarrow 0$
8: $\quad\quad$ **repeat**
9: $\quad\quad\quad j \leftarrow j + 1$
10: $\quad\quad\quad j^* \leftarrow s_{id}(j)$
11: $\quad\quad$ **until** $P_s^{\{j\}} \in P^*$
12: $\quad$ **else**
13: $\quad\quad j^* \leftarrow s_{id}(1)$
14: $\quad$ **end if**
15: $\quad$ **return** j^*
16: **end procedure**

Modeling Performance

↑ Clues for defining the IBW boundaries.

- Define I bandwidth configurations and the boundaries. (e.g., {10, 40, 80, 162, 273})
- Selects basis incrementally with BW by DOMP.
- Allowing reselection to retrain significant basis.

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5:          **repeat**
6:              $\varphi^{\{j\}} \overset{\forall j}{\leftarrow} \left| \frac{Z_i^{\{j\}}}{\|Z_i^{\{j\}}\|_2} e_i \right|$
7:              $j^* \leftarrow \text{pursuit}(M^*, M_i, M, \varphi)$
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14:          **until** stopping criterion is met
15:          $M^* \leftarrow M^* \cup M_i$
16:          $U_* \leftarrow U[M^*]$
17:          $w \leftarrow (w^T, w_i^T)^T$
18:          $e \leftarrow y - U_* w$ *<- IBW residual error*
19:          $M \leftarrow M_{\notin M_i}$ ▷ only if not allowing re-selection
20:          $i \leftarrow i + 1$
21:      **end while**
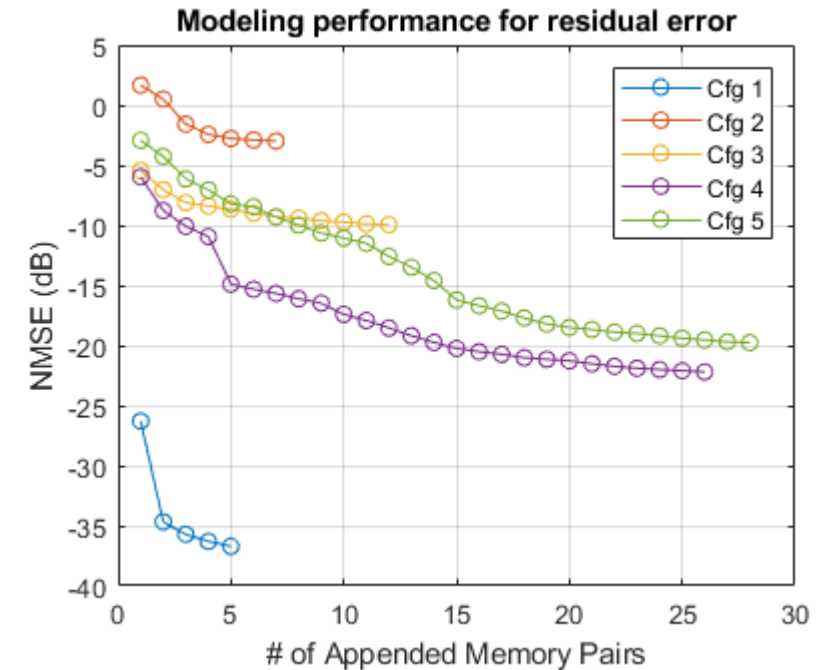22:   **return** M^*
23: **end procedure**

** The **i** here represents different BW*

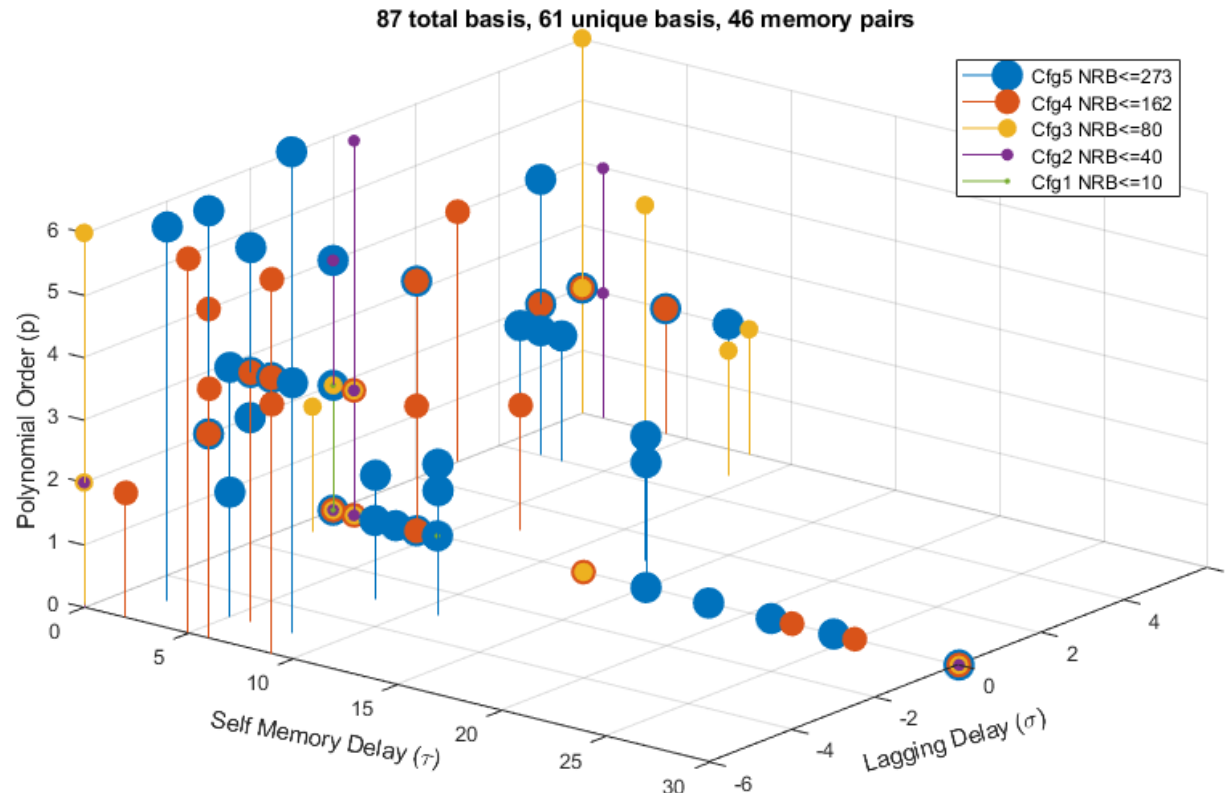**Algorithm 2** Greedy selection with memory preference

1: **procedure** PURSUIT(M^*, M_i, M, φ)
2:      $P^* \leftarrow M^{*\{\tau,\sigma\}} \cup M_i^{\{\tau,\sigma\}}, P \leftarrow M^{\{\tau,\sigma\}}$
3:      $\varphi_n \leftarrow \frac{\varphi - \min(\varphi)}{\max(\varphi - \min(\varphi))}$
4:      $\varphi_s, s_{\text{id}} \overset{\forall \varphi_n > \gamma}{\leftarrow} \text{sort}_{\text{desc}}(\varphi_n)$ *<- Score threshold*
5:      $P_s \leftarrow P^{\{s_{id}\}}$
6:      **if** $P_s \cap P^* \neq \emptyset$ **then**   *<- Memory preference*
7:          $j \leftarrow 0$
8:          **repeat**
9:              $j \leftarrow j + 1$
10:             $j^* \leftarrow s_{id}(j)$
11:          **until** $P_s^{\{j\}} \in P^*$
12:      **else**
13:          $j^* \leftarrow s_{id}(1)$
14:      **end if**
15:      **return** j^*
16: **end procedure**

Modeling performance for residual error
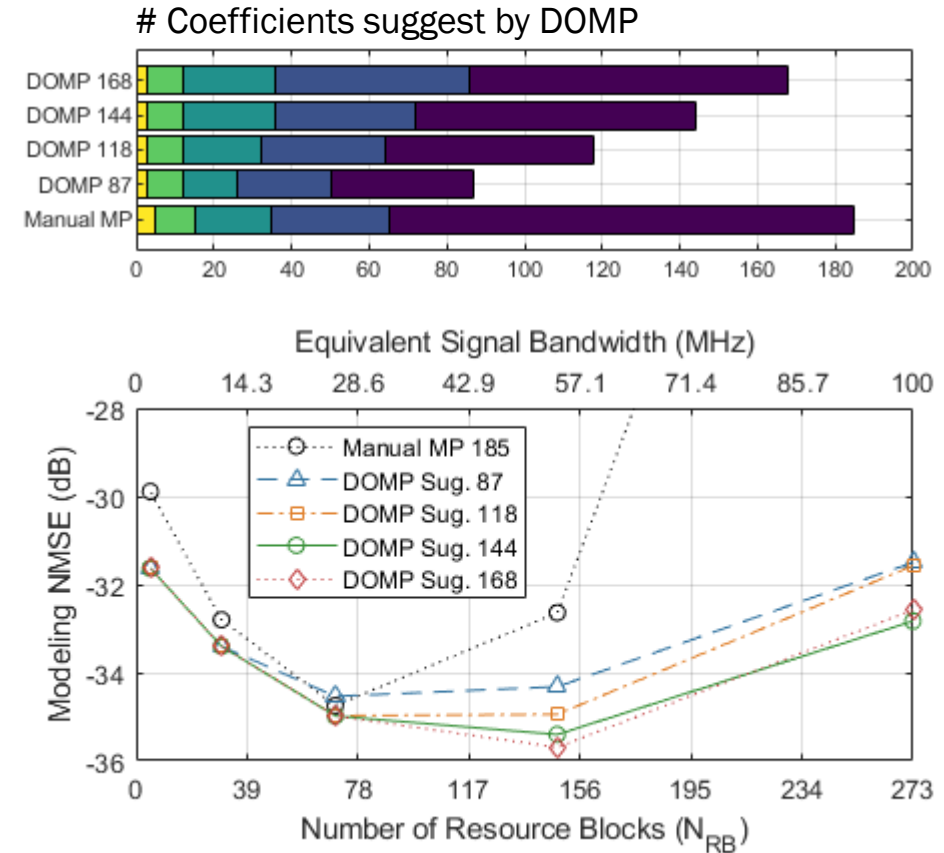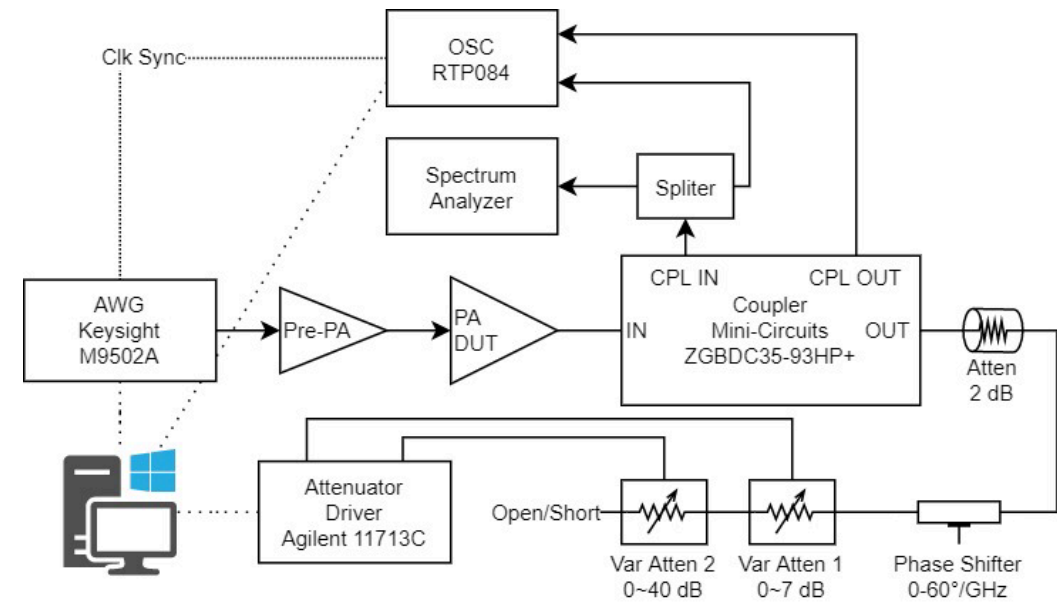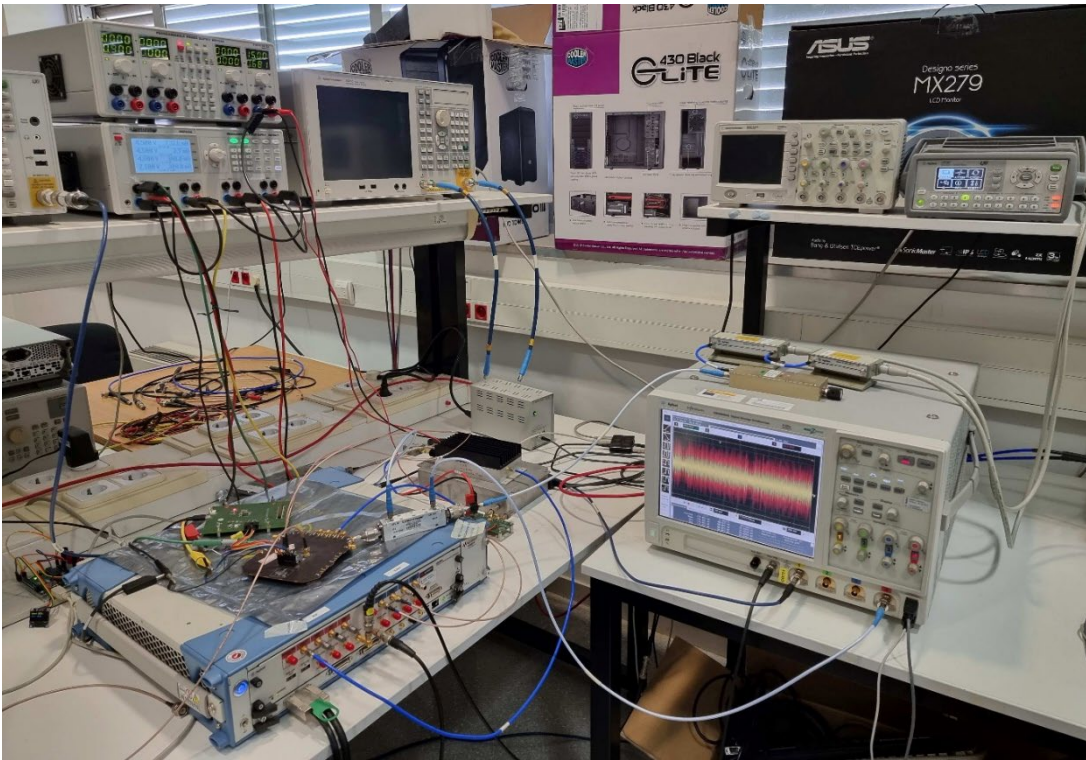(NMSE (dB) vs # of Appended Memory Pairs; Cfg 1–Cfg 5)

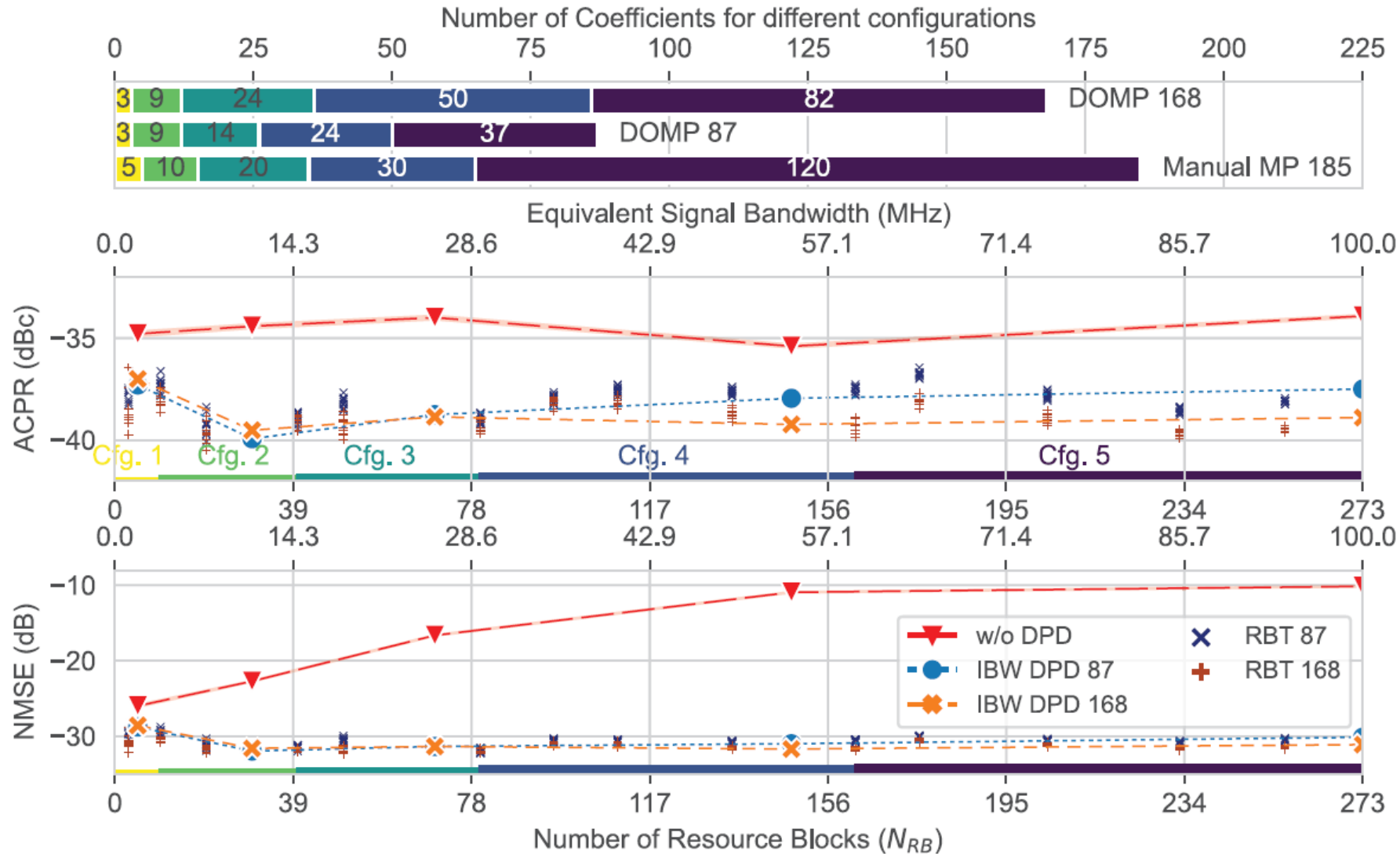- Here shows the modeling results by the IBW DOMP.

$$d_i[n] = \sum_{k_i=1}^{K_i} w_{k_i} u[n-\tau_{k_i}] \left| u[n-\tau_{k_i}-\sigma_{k_i}] \right|^{p_{k_i}}$$

- The PA DUT is a SoC solution for handset provided by Huawei Hisilicon.

- Variable attenuator controls the power of reflected wave to have a desired VSWR.

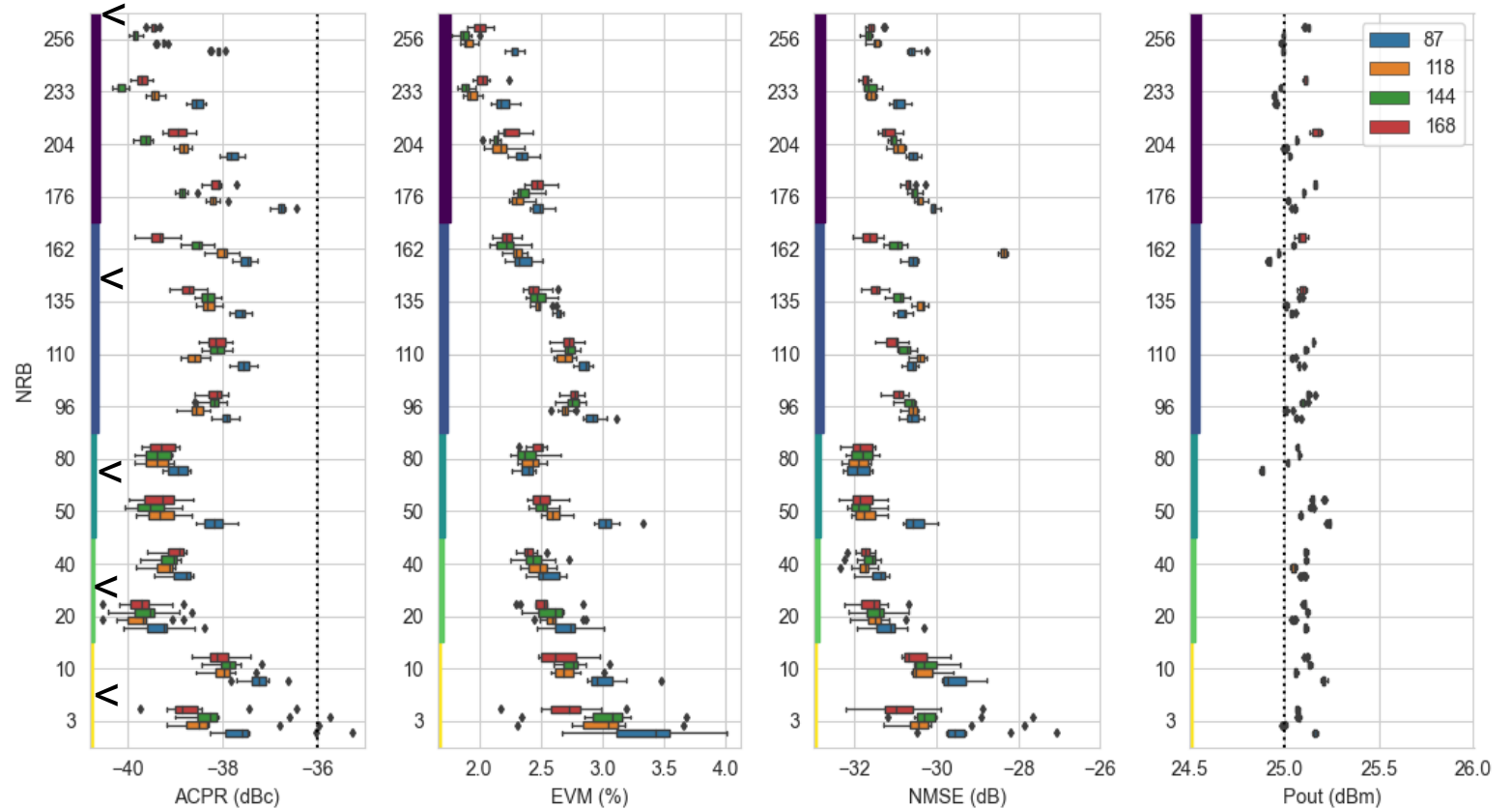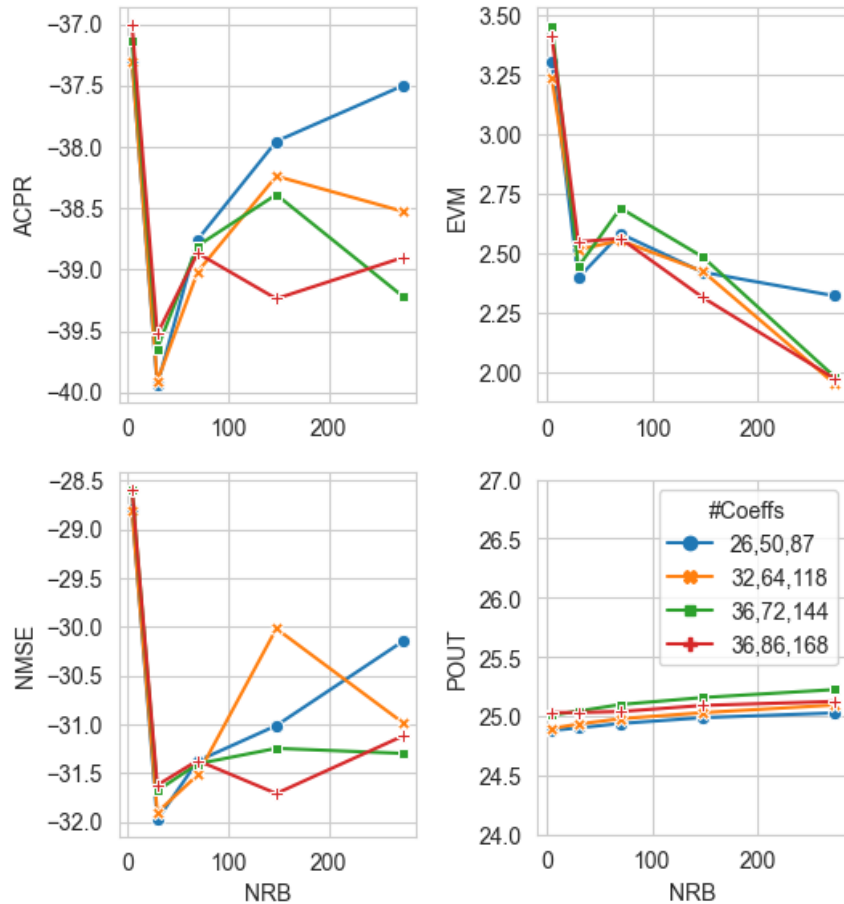- Phase shifter controls the phase of the reflected wave.

$N_{\mathrm{RB,train}} = \{5, 30, 70, 148, 273\}$

$B = \{10, 40, 80, 162, 273\}$

- Performance can be improved by using more coefficients for higher bandwidth.
- In the following test, we fix the coefficients for the first 2 configurations ({3, 9} Coeffs.).
- For the 3rd, 4th and 5th configurations, we use different number of coefficients.

Summary

- We presented the IBW DPD model.

- The constrained DOMP for IBW GMP basis selection:

  - To maximize the orthogonality between the basis and the IBW residual error.

  - Flexible to trade-off the number of basis for linearization performance.

- Tested under un-matched condition and different bandwidth.

  - The IBW DPD can work for the whole 100 MHz channel.

  - It can pass the robustness test with different signals.

- **Future work:**

  - Combine FD and IBW to support both dynamic frequency and bandwidth.

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